

## TAPERED SIGNAL LINES

ABSTRACT

Method and apparatus for a forming an integrated circuit including a plurality of conductive wires is described. A first wire having sidewalls is tapered from the proximal end to the distal end to reduce width from the first width to the second width. A second wire, spaced apart from the first wire, the second wire has sidewalls. The first wire and the second wire are each horizontally disposed along side each other forming a part of a sidewall capacitor between facing sidewalls. The sidewall capacitor capacitance is progressively reduced responsive to the first wire taper.